



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov

\*BIBDATASHEET\*

Bib Data Sheet

CONFIRMATION NO. 1912

SERIAL NUMBER 10/608,020	FILING DATE 06/30/2003	CLASS 438	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. 04329.3083
RULE				

APPLICANTS

Ichiro Mizushima, Yokohama-shi, JAPAN;

\*\* CONTINUING DATA \*\*\*\*\*

This application is a CON of PCT/JP02/08742 08/29/2002

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2001-262295 08/30/2001

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 09/23/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 1	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 4
Verified and Acknowledged	Examiner's Signature Initials				

ADDRESS

Finnegan, Henderson, Farabow,  
 Garrett & Dunner, L.L.P.  
 1300 I Street, N.W.  
 Washington , DC  
 20005-3315

TITLE

Method of purging semiconductor manufacturing apparatus and method of manufacturing semiconductor device

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other <input type="checkbox"/> Credit
RECEIVED 1330		